

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE
(REV. 7-80) PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.
010481

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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(Use several sheets if necessary)

APPLICANT

Dunworth, et al.

FILING DATE

March 1, 2002

GROUP

Unknown

DATE MAILED: March 6, 2002

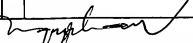
U.S. PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
	A1						
	A2						
	A3						
	A4						
	A5						
	A6						
	A7						
	A8						
	A9						
	A10						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
	B1						
	B2						
	B3						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

8/7	C1	Wilson, et al., "A CMOS Self-Calibrating Frequency Synthesizer", IEEE Journal of Solid-State Circuits, Vol. 35, No. 10, October 2000, pps 1437 - 1444.
8/7	C2	Lin, et al., "A 900-MHz 2.5-mA CMOS Frequency Synthesizer with an Automatic SC Tuning Loop", IEEE Journal of Solid-State Circuits, Vol. 36, No. 3, March 2001, pps. 424 - 431.
EXAMINER		
DATE CONSIDERED		1/7/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

STATEMENT BY APPLICANT
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010481SERIAL NO.
10/092,669APPLICANT
Dunworth, et al.FILING DATE
3/6/02GROUP
Unknown 2685

DATE MAILED: August 9, 2004

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
BK	A1	2 0 0 2 0 0 7 9 9 7 3	6/27/02	Higashi, et al.			11/20/01
BK	A2	2 0 0 2 0 0 3 6 5 4 5	3/28/02	Fridi, et al.			8/13/01

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
	A3	5 6 0 4 4 6 5	2/18/97	Farabaugh			
	A4	5 1 8 2 5 2 8	01/26/93	Zuta			
	A5						

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FOREIGN PATENT DOCUMENTS

Technology Center 2600

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
BK	B1	1 1 6 8 6 2 7	01/02/02	EPO	Matsushita Electric, etc., et al.		
BK	B2	9 9 0 4 4 9 5	01/28/99	WO	Microchip, etc., et al.		
	B3						
	B4						

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C1	
C2	
C3	

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Sheet 2 of 3

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